

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

**Title of  
Invention**

**AUTOMATIC BIT FAIL MAPPING FOR EMBEDDED  
MEMORIES WITH CLOCK MULTIPLIERS**

Application Number :           **10/707071**

Confirmation Number:

First Named Applicant:       **Darren Anand**

Attorney Docket Number:     **BUR920030168US1**

Art Unit:                       **2117**

Examiner:                      **KERVEROS, JAMES**

Search string:                 **( 4876685 or 5936876 or 6185709 or 6509766 or 0084387 ).pn**

## US Patent Documents

**Note: Applicant is not required to submit a paper copy of cited US Patent Documents**

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
/JK/	1	4876685	1989-10-24	Rich			
	2	5936876	1999-08-10	Sugasawara			
	3	6185709	2001-02-06	Dreibelbis et al			
	4	6509766	2003-01-21	Pomichter et al			
	5	0084387	2003-05-01	Rooney et al			

**Signature**

**Examiner Name**

**Date**

/James Kerveros/

03/20/2007

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /JK/